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In application of: Amir Alon, et al.

IBM Docket No.: IL920020007US1

Serial No.: 10/091,934

Group Art Unit: 2825

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Title: INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED
CIRCUIT DESIGN

Commissioner for Patents

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CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this paper (13 pages), Appendix A and Appendix B (2 pages) and Petition for Extension for Time (one month) – total (16 pages) is being facsimile transmitted under Rule 37 CFR 1.6(d) to the U.S. Patent and Trademark Office to (703) 872-9306 on March 3, 2005.



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Amendment

Sir:

This amendment is filed in response to the Office Action dated 04 Nov 2004 issued by the USPTO in connection with the above-identified patent application. A response to the 04 Nov 2004 Office Action was due 04 February 2005. Applicants are concurrently filing a Petition for a one-month extension of time, including the required fee. Therefore a response is due 04 March 2005. Accordingly, this Amendment is being timely filed.

The listing of claims begins on page 3 of this paper. Remarks begin on page 8 of this paper. This listing of claims will replace all prior versions including the listing of claims in the application.

Serial No. 10/091,934

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